

## Speaker Profile



### Contact Details

**Organization**  
Name: School of  
EECS, Kyungpook  
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**Jong-Ho Lee**

**Associate Professor**

**Kyungpook National University**

### EDUCATION BACKGROUND

Ph.D., Electronics Engineering, Seoul National University, Aug. 1993. (Solid State Materials, Device and Technology)

M.S., Electronics Engineering, Seoul National University, Feb. 1989. (Semiconductors)

B.S., Electronics Engineering, Kyungpook National University, Feb. 1987.

### Current Research Topics

- (1) Sub-50 nm CMOS device technology (inventor of bulk FinFETs)
- (2) 70 nm NAND flash technology
- (3) Nano-Floating Gate Memory flash devices
- (4) High resolution/sensitivity CMOS image sensor using nano CMOS
- (5) Microsystems: 3-D CMOS devices and 3-D IC
- (6) Smart sensors
- (7) Device reliability
- (8) RF modelling of bulk FinFETs and RF circuit design
- (9) Wafer bonding/etch-back and micro-probe for IC test using MEMS technology

### PROFESSIONAL WORK EXPERIENCE

- 03/'03-present Deputy director of National Education Center for Semiconductor Process Technology, Kyungpook National University
- 02/'02-present Associate Professor, Kyungpook National University
- 03/'01-02/'04 A chairman of Device Technology, Reliability & Modeling section, Korean Conference on Semiconductors
- 10/'03-11/'03 Committee member of preparation of Next Generation National Growth Engine (nano-scale semiconductor technology section), the Ministry of Science and Technology
- 08/'02-11/'02 Committee member of preparation of National Technology Roadmap (nano-scale semiconductor devices and new devices section), the Ministry of Science and Technology
- 1/'00-1/'01 Lecturer on Fundamental Device Physics, Samsung Electronics
- 8/'98-7/'99 Massachusetts Institute of Technology, Postdoctoral Researcher, Ultimate limits of silicon based CMOS devices
- 3/'94 – 02/'02 Wonkwang University, Korea, Full-time Instructor, Assistant Professor, Associate Professor
- 11/'94 – 05/'98 Electronics and Telecommunications Research Institute, Korea, Invited Researcher

# *Center Information*

## **Center Name**

**CONTACT**

**The Center**

**For further  
information  
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General information including funding source, annual budget, research area, personnel, etc.

**Contact name**

**Position**

**Organization Name**

**Address**

Phone:

Fax:

Email:

Website:

**PLATFORM  
TECHNOLOGIES**